

FIG. 1

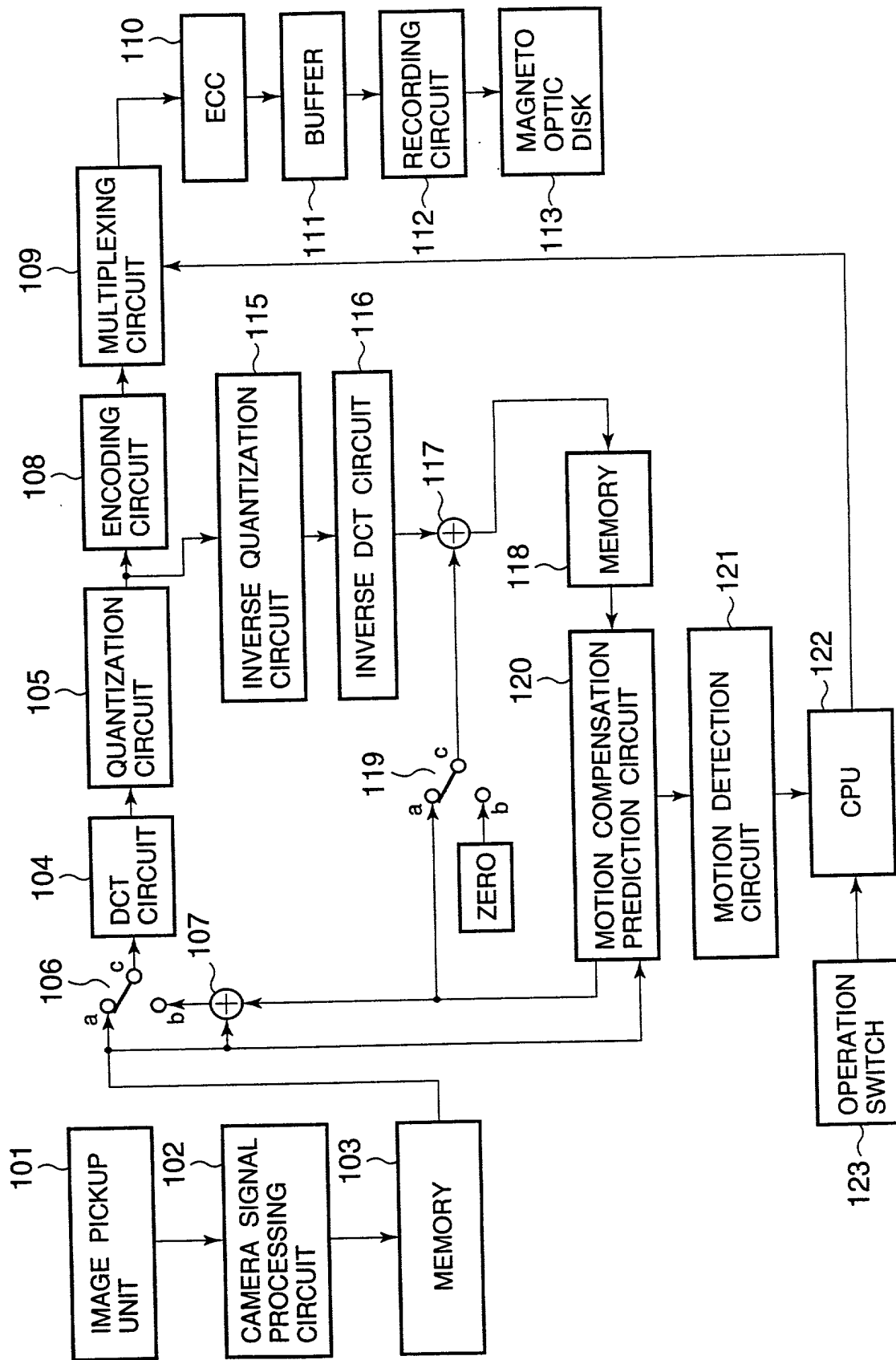


FIG. 2

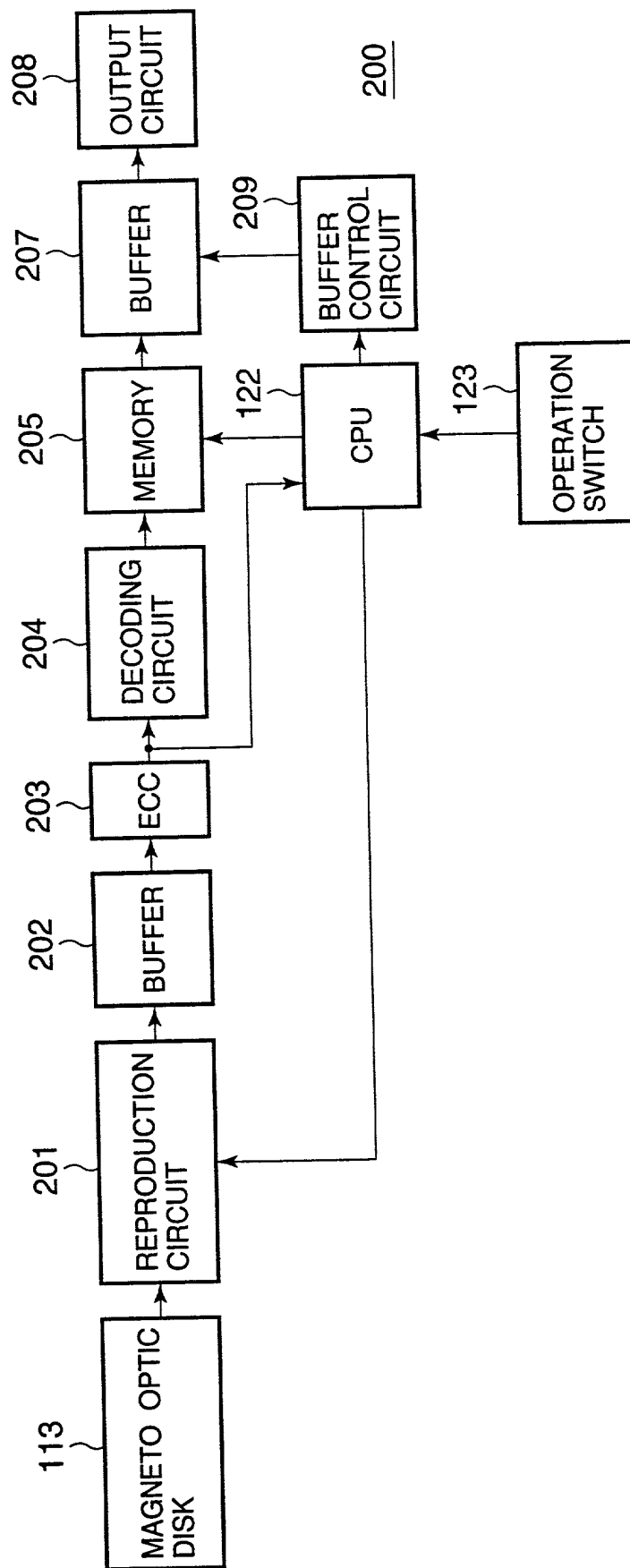
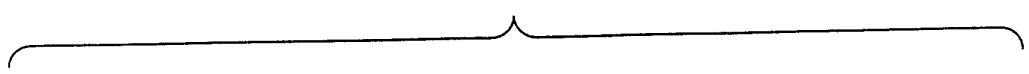
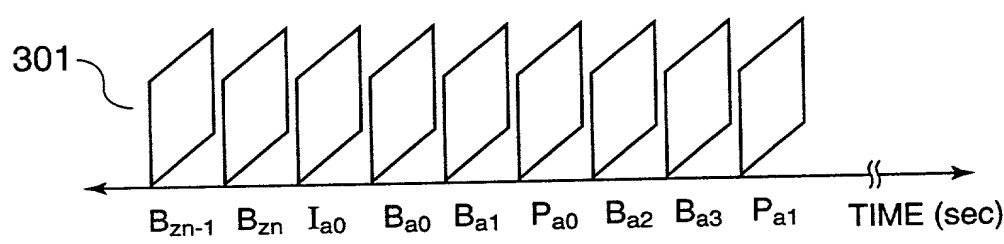


FIG.3



MEMORY INPUT (FRAME IMAGE)



MEMORY OUTPUT (FRAME IMAGE)

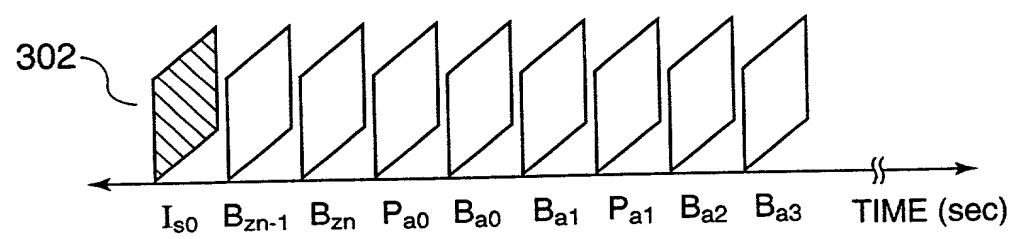


FIG.4

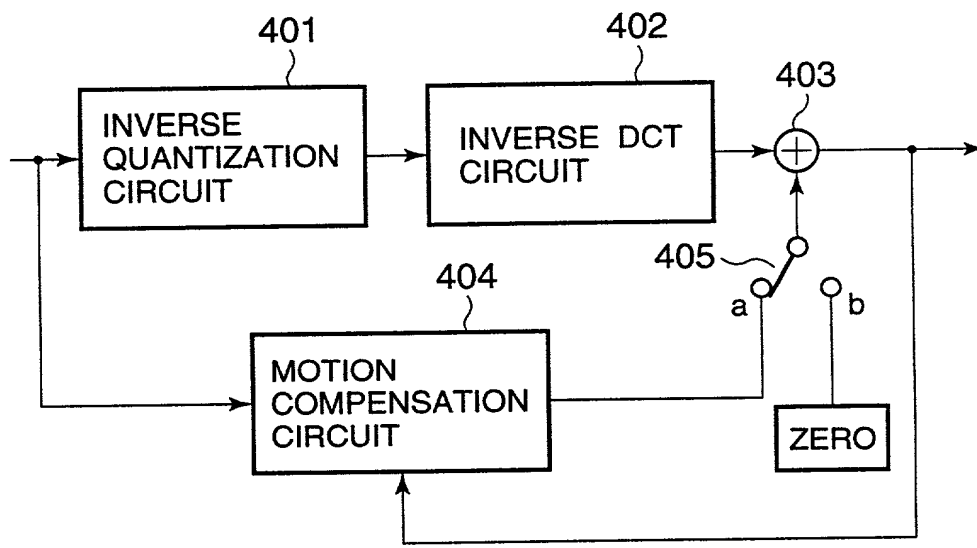
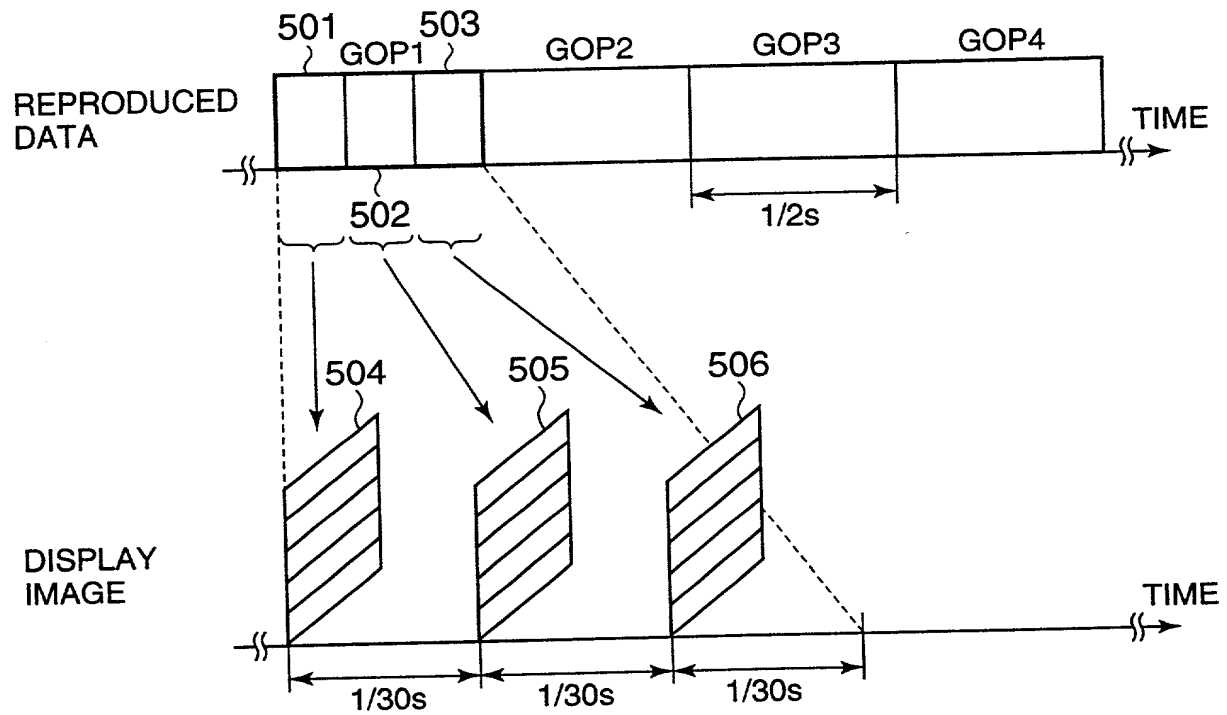


FIG.5



# FIG.6

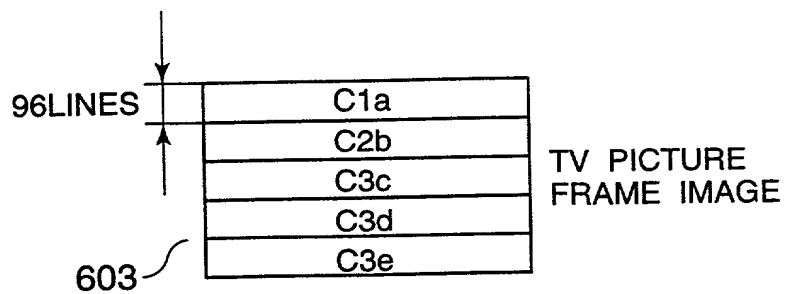
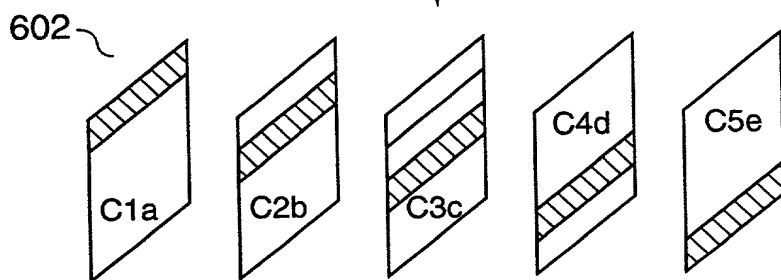
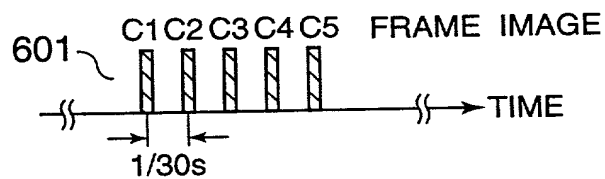
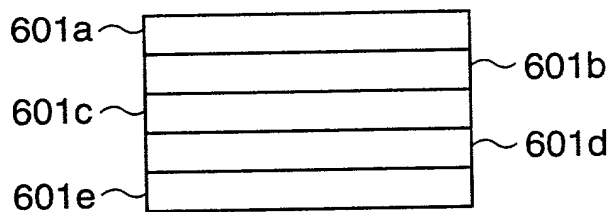


FIG.7

